



2152#3

P A T E N T

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
)  
R. Petrovic )  
)  
Application No.: 10/066,039 )  
)  
Filed: January 31, 2002 )  
)

**RECEIVED****APR 11 2002****Technology Center 2100**

For: **METHODS AND APPARATUS FOR EMBEDDING AND RECOVERING  
WATERMARKING INFORMATION BASED ON HOST-MATCHING CODES**

Commissioner for Patents  
Washington, D.C. 20231

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail, postage prepaid, in an envelope addressed to Commissioner for Patents, Washington, D.C. 20231 on April 3, 2002

By: Carol Prentice  
Carol Prentice

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

In accordance with 37 C.F.R. §1.56, Applicant wishes to call the attention of the Examiner to the following references:

U.S. PATENTS:

<u>Patent No.</u>	<u>Patentee</u>	<u>Date of Issue</u>
5,940,135	Petrovic, et al.	August 17, 1999
5,450,490	Jensen, et al.	September 12, 1995
5,319,735	Preuss, et al.	June 7, 1994
5,113,437	Best, et, al.	May 12, 1992
4,876,617	Best, et, al.	October 24, 1989
5,613,004	Cooperman, et al.	March 18, 1997
5,822,360	Lee, et al.	October 13, 1998
5,581,800	Fardeau, et al.	December 3, 1996
5,764,763	Jensen, et al.	June 9, 1998

5,687,191	Lee et, al.	November 11, 1997
5,073,925	Nagata, et al.	December 17, 1991
5,893,067	Bender, et al.	April 6, 1999
5,579,124	Aijala, et al.	November 26, 1996
5,828,325	Wolosewicz, et al.	October 27, 1998

**PUBLICATION(S) :**

1. A.V. Kuznetsov, et al., "An Error Correcting Scheme for Defective Memory", IEEE Trans. Inf. Theory, vol. IT-24, no. 6, pp. 712-718 (Nov. 1978) (7 pages).
2. C. Heegard, et al., "On the Capacity of Computer Memory with Defects", IEEE Trans. Info. Theory, vol. IT-29, no. 5, pp. 731-739 (Sep. 1983) (9 pages).
3. M. Costa, "Writing on Dirty Paper" IEEE Trans. On Info. Theory, IT-29, no. 3, pp. 439-441 (May 1983) (3 pages).
4. J. Chou, et al., "A Robust Optimization Solution to the Data Hiding Problem using Distributed Source Coding Principles", Pro. SPIE, Vol. 3971, San Jose, CA (Jan. 2000) (10 pages).

Copies of the above-listed references are enclosed, with a completed form PTO-1449.

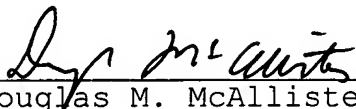
Consideration of these references by the Examiner is respectfully requested. This Information Disclosure Statement is being filed within three months of the patent application filing date. Moreover, to date, a first Office Action on the merits has not been received in connection with this application. This

Application No.: 10/066,039  
Page 3

Information Disclosure Statement is therefore believed to be  
timely filed and no filing fee is due.

Respectfully submitted,

Date: April 3, 2002  
ATTORNEY DOCKET NO.: SOL-166

  
\_\_\_\_\_  
Douglas M. McAllister  
Attorney for Applicant(s)  
Registration No. 37,886  
Law Office of Barry R. Lipsitz  
755 Main Street, Bldg. 8  
Monroe, CT 06468  
(203) 459-0200



SHEET 1 OF 1

FORM PTO-1449

ATTY. DOCKET NO.: SOL-166

SERIAL NO.:  
10/066,039**INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

APPLICANT(S): R. Petrovic

FILING DATE: January 31, 2002

GROUP:

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLA SS	SUB- CLASS	FILING DATE
	AA	5,940,135	August 17, 1999	Petrovic, et al.			
	AB	5,450,490	Sept. 12, 1995	Jensen, et al.			
	AC	5,319,735	June 7, 1994	Preuss, et al.			
	AD	5,113,437	May 12, 1992	Best, et al.			
	AE	4,876,617	October 24, 1989	Best, et al.			
	AF	5,613,004	March 18, 1997	Cooperman, et al.			
	AG	5,822,360	October 13, 1998	Lee, et al.			
	AH	5,581,800	December 3, 1996	Fardeau, et al.			
	AI	5,764,763	June 9, 1998	Jensen, et al.			
	AJ	5,687,191	November 11, 1997	Lee, et al.			
	AK	5,073,925	December 17, 1991	Nagata, et al.			
	AL	5,893,067	April 6, 1999	Bender, et al.			
	AM	5,579,124	November 26, 1996	Aijala, et al.			
	AN	5,828,325	October 27, 1998	Wolosewicz, et al.			

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)**

	AO	A.V. Kuznetsov, et al., "An Error Correcting Scheme for Defective Memory", IEEE Trans. Inf. Theory, vol. IT-24, no. 6, pp. 712-718 (Nov. 1978) (7 pages).
	AP	C. Heegard, et al., "On the Capacity of Computer Memory with Defects", IEEE Trans. Info. Theory, vol. IT-29, no. 5, pp. 731-739 (Sep. 1983) (9 pages).
	AQ	M. Costa, "Writing on Dirty Paper" IEEE Trans. On Info. Theory, IT-29, no. 3, pp. 439-441 (May 1983) (3 pages).
	AR	J. Chou, et al., "A Robust Optimization Solution to the Data Hiding Problem using Distributed Source Coding Principles", Pro. SPIE, Vol. 3971, San Jose, CA (Jan. 2000) (10 pages).

**EXAMINER****DATE CONSIDERED**

**EXAMINER:** Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.